

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10001478
	Filing Date	2001-11-01
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Jason Scott Proctor
	Attorney Docket Number	CD01213M

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Attorney Docket Number	CD01213M	

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/JP/	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>

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Examiner Signature	/Jason Proctor/	Date Considered	07/30/2009
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	33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block;" Mar; 46 pages	<input type="checkbox"/>
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Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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Examiner Name	Jason Scott Proctor
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First Named Inventor	Craig Nemecek	
Art Unit		2123
Examiner Name	Jason Scott Proctor	
Attorney Docket Number		CD01213M

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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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/JP/	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
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/JP/	41	USPTO Non-Final Rejection for Application Number 09/912,768 (CD00200) dated 06/22/2004; 11 pages	<input type="checkbox"/>
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/JP/	6	Varma et al., "A Structured Test Re-Use Methodology for Core-Based System Chips," IEEE, 1998; 9 pages	<input type="checkbox"/>
/JP/	7	Andrews, "Roadmap for Extending IEEE 1149.1 for Hierarchical Control of Locally-Stored, Standardized command Set, Test Programs," IEEE, 1994; 7 pages	<input type="checkbox"/>

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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

	8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE, 8 pages	<input type="checkbox"/>
/JP/	9	Ghosh et al., "A Low Overhead Design for Testability and Test Generation Technique for Core-based Systems," IEEE, 1997; 10 pages	<input type="checkbox"/>
/JP/	10	Zorian, "Test Requirements for Embedded Core-based Systems and IEEE P1500," IEEE, 1997; 9 pages	<input type="checkbox"/>
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/JP/	14	Marsh, "Smart Tools Illuminate Deeply Embedded Systems," EDN, 2000; 7 pages	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
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(Not for submission under 37 CFR 1.99)

Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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Examiner Signature	/Jason Proctor/	Date Considered	07/31/2009
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	Art Unit	2123
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